

i.MX 7Dual Board with TI PMIC

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Overview

i.MX 7Dual Board with TI PMIC is used for high-performance processing with low-power requirements, incorporating high degree of functional integration. The fully functional board integrate TI PMIC, TPS6521815 with NXP i.MX 7 Dual Application Processor. The design consists of DDR3L SDRAM (2x512MB), 64MB Serial NOR Flash, 8GB eMMC 5.0 iNAND, SD Card interface v3.0, 50 pin LCD Connector for external TFT display interface, 1000Base-T Ethernet, USB2.0 Type A and micro-AB, MIPI CSI for external camera interface and mini-PCIe interface.

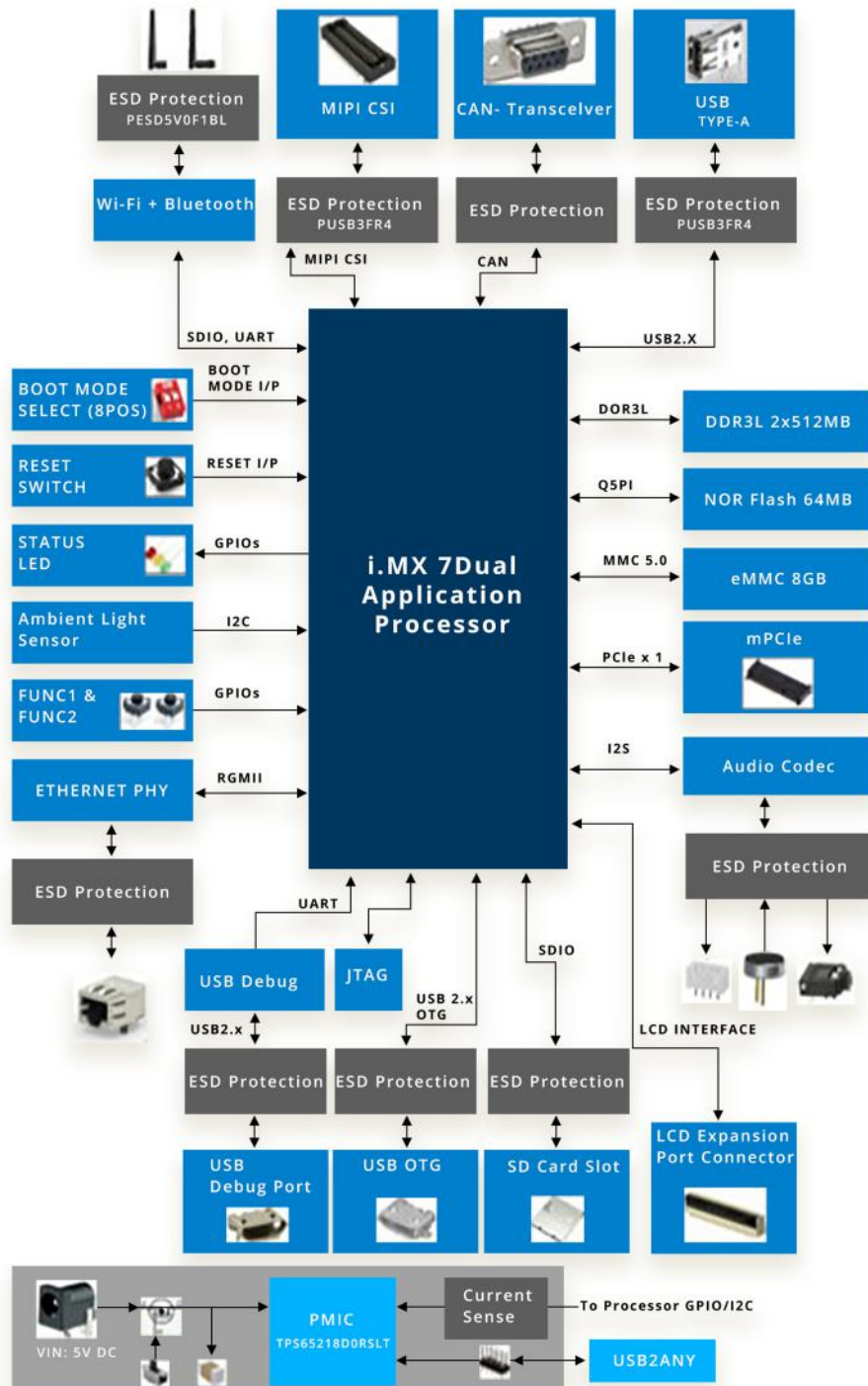
Key Features

- Ethernet 1000Base-T
- Wi-Fi IEEE 802.11 b/g/n
- Bluetooth 4.2
- I.MX 7Dual Applications Processor
- TI PMIC - TPS6521815
- DDR3LSDRAM - 2x512MB

Applications

- EPOS
 - Barcode scanner
- Factory Automation
 - Machine vision
- Medical Equipment

Hardware Architecture Diagram



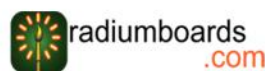
Technical Specifications

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- ☒ I.MX 7Dual Applications Processor
 - 2xArm Cortex-A7 Cores, 32-bit, 1GHz
 - Arm Cortex-M4 Core, 32 bit, 200MHz
 - NEON MPE Coprocessor
 - 512KB unified I/D L2 cache
- ☒ TI PMIC - TPS6521815
- ☒ DDR3LSDRAM - 2x512MB
- ☒ eMMC 5.0 - 8GB
- ☒ Serial NOR Flash - 64MB
- ☒ SD Card
- ☒ Audio Codec
- ☒ Ethernet 1000Base-T
- ☒ Wi-Fi IEEE 802.11 b/g/n
- ☒ Bluetooth 4.2
- ☒ USB to UART for debug
- ☒ CAN
- ☒ Ambient Light Sensor
- ☒ USB2ANY Interface
- ☒ JTAG Interface
- ☒ Multi-Function switches & Status LEDs
- ☒ Current Sensing Circuit for PMIC power out rails.
- ☒ Processor BOOT Mode selection
- ☒ USB 2.0 Type A
- ☒ USB micro-AB with OTG
- ☒ LCD - 24 pin Parallel Interface
- ☒ MIPI CSI
- ☒ mini-PCIe

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